A Modular Architecture for the Development of
Online Social Networks Simulators based on
Chainable Processing Modules

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Abstract

Working with Complex Networks simulations focused on Online Social Networks (OSNs), we reached the limits of the architectural features of our software tool, because its project is not suitable to the inclusion of new resources as dynamic topological operations, on-the-fly visualizations, recognition of yet-to-be-developed algorithms, and concurrency programming support. We analyzed our current simulator and prepared a requirement list target to the development of a new one, privileging two features: flexibility and extensibility. We developed and applied the concept of independent processing modules that can form a processing chain according to Design Patterns and Java interface standards. These Chainable Processing Modules are the novelty and foundation of our architecture, that complies very well with the new simulator requirements. In this work, we present our software architecture and highlight its potential to help the development of open projects of simulators of any type of Complex Networks.

Keywords: Simulation, Social Network Analyses, Software Architecture, Computational Data Analysis and Simulation in General Sciences.

1. INTRODUCTION

Computational simulation is an important tool for the study of complex networks in general [1, 2], and therefore it has a highlighted role to understand Online Social Networks (OSNs). These networks have been intensely researched in recent years, given their importance in today’s world, as one can find in the following examples: [3, 4, 5, 6].

While we were working on a special case of partition of networks [7], we developed a piece of software that simulates the operation of common OSNs, named Demortuos[8]. Its main goal is the assessment of candidate
algorithms of partitioning and it has an iterative graphical user interface, as exemplified in Fig. 1.

Briefly, the program sequentially generates one of the possible realizations of the network according to a user-selected model, simulates a number of message exchanges and operation days, and applies the algorithms under evaluation, collecting data for their posterior analyses. It has two available models: i) the Random Graph model after [9]; ii) the Scale-free model after [10]. Demortuos has built in Java, following the Design Patterns [11]. We specially used Strategy and Factory to improve its flexibility and assesses three algorithms of partitioning. Because of its sequential processing, practical simulations are restricted to about 50,000 nodes per network.

Although the program very well accomplishes its job, it is the time when some new research demands have arouse, bringing the software to its limits. For instance, currently investigations need more sophisticated models that include community structures because it changes the network dynamics. A review on this subject is found in [12]. Also, rewiring schemes need to be considered in the investigations because OSNs undergo changes in their links. Some considerations about rewiring process are found in [13, 14, 15]. Furthermore, we foresee new models for on-fly topology changes that must be researched.

Therefore, we started the project of a new piece of software, for now code-named DemortuosNG, that is intended to completely replace the pre-
Table 1: New architecture requirements

<table>
<thead>
<tr>
<th>#</th>
<th>Requirement</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>Very flexible general-purpose simulator for OSNs</td>
<td>mandatory</td>
</tr>
<tr>
<td>02</td>
<td>Automated simulation of several scenarios</td>
<td>mandatory</td>
</tr>
<tr>
<td>03</td>
<td>Compliance with Design Patterns standards</td>
<td>mandatory</td>
</tr>
<tr>
<td>04</td>
<td>Compliance with Java 8 standards</td>
<td>mandatory</td>
</tr>
<tr>
<td>05</td>
<td>Compliance with Javadoc standards</td>
<td>mandatory</td>
</tr>
<tr>
<td>06</td>
<td>Ability to deal with static and dynamic network models</td>
<td>mandatory</td>
</tr>
<tr>
<td>07</td>
<td>Acceptance of new network models</td>
<td>mandatory</td>
</tr>
<tr>
<td>08</td>
<td>Acceptance of new network operation patterns</td>
<td>mandatory</td>
</tr>
<tr>
<td>09</td>
<td>Recognition of yet-to-be-developed assessment algorithms</td>
<td>mandatory</td>
</tr>
<tr>
<td>10</td>
<td>Integrated simulation of topology evolution and network operation</td>
<td>mandatory</td>
</tr>
<tr>
<td>11</td>
<td>Entry points to pre- and post-processing algorithms</td>
<td>mandatory</td>
</tr>
<tr>
<td>12</td>
<td>Entry points to filters and general-purpose algorithms</td>
<td>mandatory</td>
</tr>
<tr>
<td>13</td>
<td>Entry points to on-the-fly data collection and storage</td>
<td>mandatory</td>
</tr>
<tr>
<td>14</td>
<td>Entry points to on-the-fly visualization</td>
<td>mandatory</td>
</tr>
<tr>
<td>15</td>
<td>Multithread support</td>
<td>mandatory</td>
</tr>
<tr>
<td>16</td>
<td>Interface with third-part programs</td>
<td>optional</td>
</tr>
<tr>
<td>17</td>
<td>Iterative and batch operation modes</td>
<td>optional</td>
</tr>
<tr>
<td>18</td>
<td>Parallel processing support</td>
<td>optional</td>
</tr>
</tbody>
</table>

Previous tool. We target a full OSN simulator, not only a tester for partitioning algorithms. So, the requirements undergone a significant change, as well as the software architecture. In this paper, we present the proposed software architecture as it brings a new approach to the design of this type of simulator. A point to be noticed is the chainable processing modules. These modules permit that an undefined number of processing steps be combined in an arbitrary and extensible fashion, pushing the flexibility of the simulator to the next level. In the next section, we present the methodology for the design of this new architecture.

2. METHODS

The objective of the new program is to produce a general-purpose OSN simulator, because we look for a flexible tool, able to process several network topology models under a diversity of operation patterns, algorithms, and assessment procedures. Iterative and batch mode are desired features, as well the support to execution threads and concurrency. Consequently,

\[\text{Here, we adopt the [16]'s approach where threads optimize the time of one single CPU, whereas the concurrency concerns to a multicore/multi-CPU computer.}\]
we started with a requirement analyses that leaded us towards a list of mandatory and optional features to the new simulator, as summarized in Table 1.

From the requirements, we overlooked a whole new design to the simulator. Its main feature is the use of a set of chainable modules inspired by the Chain of Responsibility Design Pattern[11]. These modules are intended to implement a common Java interface in order to be fully interchangeable. This design provides the simulator with entry points for a yet-to-be-developed set of network models, filters, and algorithms, as stated in the requirements of the system. The detailed proposal is shown in the next section.

3. THE ARCHITECTURE

The proposed architecture for DemortuosNG is shown in Fig. 2. Each box indicates a module or a set of modules, whereas the arrows define the main expected information flow. The Control module makes the overall coordination of the system and exchanges pieces of information with virtually all other modules, but we omit its arrows to get a clean drawing. We highlight that there is a relation among the modules and Java classes or interfaces, but this is not a one-to-one map. Some modules may need several classes to their implementation, whereas two modules may be jointed in one class according to implementation decisions.

There are three modules groups: core simulation, input, and output. They are intended to organize the architecture. The first one guides the program concurrency, whereas the other two impose the production of a Java interface to accommodate interchangeable modules, including the adapters to communicate with other applications. In Fig. 2, the Chainable Processing module is only a placeholder to the actual ones. Next, we present the functionality of each module.

Input Group: all the modules of this group must implement a common interface in order to be interchangeable; they are responsible to get configuration data such as simulation scenarios from diverse sources, as follows:

- Storage: from plain text files or database systems;
- GUI: from an interactive graphical user interface;
- Batch: from command line arguments or scripts files;
- Adapter: from a third-part application.

Output Group: all the modules of this group must implement a common interface in order to be interchangeable; they are responsible to collect data from the simulation process and present or save it to a destination as follows:
Figure 2: Overview of proposed architecture to OSNs simulators, presenting the structural modules; Chainable Processing is a placeholder to the actual modules; as Control exchanges data with all modules, its arrows were omitted.

- Storage: to plain text files or database systems;
- Viewer: to an interactive graphical user interface;
- Reporter: to a structured high-level document;
- Adapter: to a third-part application.

Core Simulation Group: the modules of this group execute the processing steps of the simulation.

- Pre-processing: the one-time run procedures previous to the simulation, if any;
- Simulation: the simulation processing itself, according to the sequenced chain of processing modules;
- Post-processing: the one-time run procedures posterior to the simulation, if any.

Loose modules: they have different roles as follows.
• Starting: system initialization tasks only concerned to the program;
• Workload Setup: load and configuration of the scenarios to be simulated;
• Simulation Environment Setup: setup of the run conditions, concurrency mechanism, and correlated tasks;
• Network Generator: creation of one network realization according to the chosen workload;
• Control: overall coordination of the system;
• Processing Chain Setup: creation of a sequence of chainable processing modules to be submitted to the Core Simulation;
• Chainable Processing: a placeholder for the actual modules (Fig. 3).

The Chainable Processing Modules are processing elements that may be arranged in a chain sequence in order that the same object can be inspected and processed by all of them. They are the key point of our architecture, because they can include almost all types of algorithms in the simulator in a standardized way. One network object pass through the chain in each iteration undergoing different processing routines. The foreseen modules are shown in Fig. 3 as follows.

• Filters: data selection, transformation, and conversion;
• General Algorithms: all-purpose processing;
• Internal Verification: integrity check for the simulation;
• Network: structural processing over network, as follows:
  – Characterization: topological evaluation \(^3\);
  – Topology Dynamics: topological changes, rewiring;
• Assessment Algorithms: assessment of researches subjects;
• Operation: OSNs dynamics, as follows:
  – Network Dynamics: non-topological changes;
  – User Dynamics: user behavior or status changes;
  – Message Dynamics: message exchange simulation.

Next, we discuss how the proposed architecture complies with the requirements.

• Very flexible general-purpose simulator for OSNs: the architecture was designed to perform all-purpose simulations of OSNs by means of the chainable modules that provide the desired level of flexibility; the input module permits a series of different sources for the configuration of

\(^3\)See [17] for further details about characterization of complex networks.
the simulator, whereas the output modules are intended to be extensible Java classes, able to provide several forms to show the processing results, even for the intermediate ones.

- Automated simulation of several scenarios: the architecture was planned to support multiple instances of the core simulation module; it is intended to automatically run several scenarios without user intervention.

- Compliance with Design Patterns, Java 8, and Javadoc standards: although the compliance is deeply related to the detailed project and codification of Java classes, the proposed architecture fully supports these standards.

- Ability to deal with static and dynamic network models: the independence among the network generator modules and the simulation core allows the system works with the both types of networks.

- Acceptance of new network models and operation patterns: the use of independent extensible modules for generation, evolution, and simulation of networks provides this compliance.

- Recognition of yet-to-be-developed filters, general-purpose and assessment algorithms: the chainable modules may be extended to imple-
ment a new functionality; thus, it is possible to include new code in a standardized way as soon it becomes available.

- Integrated simulation of topology evolution and network operation: the architecture does not impose the separation between these two tasks as in the previous software tool, so it is possible to integrate them according to the user needs.

- Entry points to pre- and post-processing algorithms: they are located in the Core Simulation group, as specific processing objects.

- Entry points to on-the-fly data collection, visualization, and storage: the chainable extensible output modules provide support for these required operations.

- Multithread and parallel processing support: the core simulation module may be parallelized to simultaneously run different scenarios and/or repetitions related to a configuration set of parameters; the actual implementation will choose the most suitable technique.

- Interface with third-part programs: there is a prevision for Adapters in both input and output modules, then it is possible to write interface code to other programs.

- Iterative and batch operation modes: this feature is supported by the independence between the input modules and the processing ones.

4. CONCLUSION

We proposed a software architecture to OSNs simulators, based in our previous experience with this type of program. As our goal is to produce a general-purpose OSN simulator, we prepared a list of ambitious requirements for the software development, which are as general as possible, but they presuppose an actual object oriented implementation. Although we are going to make our code using Java, the architecture is suitable for C++ as well.

We presented a new, modular, flexible, and extensible software architecture that has chainable processing modules as its strongest novelty. These modules impose a particular model of operation to the simulator, that supports the inclusion of a yet-to-be-developed functionality set. Thus, our architecture may be used to create open OSNs simulators and to inspire the design of other simulators.

Currently, we are validating the proposed architecture and starting the project of the actual Java classes, while our research group finishes a test
case set. We intended to have the production version of the DemortuosNG by the end of 2017.

References


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